

	Combinatorial Logic	Fully Pipelined	Optimized for 100Gbps	Lin et al. (2017)
Technology	28 nm	28 nm	28 nm	28 nm (scaled from 90 nm)
Code	128/64	128/64	128/64	1024/512
Architecture	Unrolled	Unrolled	Unrolled	Sequential
Throughput (Gbps)	6.39	121.9	108.5	2.5
Max. Frequency (MHz)	49.9	952.4	847.5	1440
Nr. of Reg. Stages	0	64	24	–
Total Size of Reg. (KB)	0	13.9	5.23	–
Area (mm ²)	0.36	0.76	0.39	0.12
Area Efficiency (Gbps mm ^{−2})	17.7	159.6	276.0	20.5