

	Greshishchev et al. (2011)	Huang et al. (2014)	Schuh et al. (2016)	Cao et al. (2017)	This work
Technology	65 nm CMOS	28 nm CMOS	55 nm BiCMOS	20 nm CMOS	28 nm CMOS
Nominal DAC resolution (bit)	6	8	6	8	8
DAC conversion rate (GS s^{-1})	56	100	100	64	64 (simulated*)
Time interleaving	no	yes (4 \times)	no	no	no
Last MUX stage output (Gbit s^{-1})	56	25	100	64	64 (simulated*)